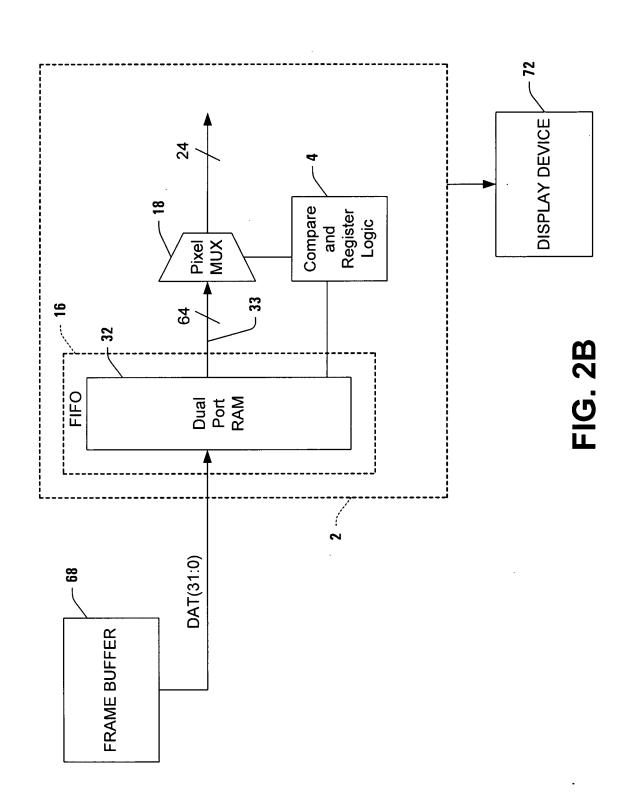
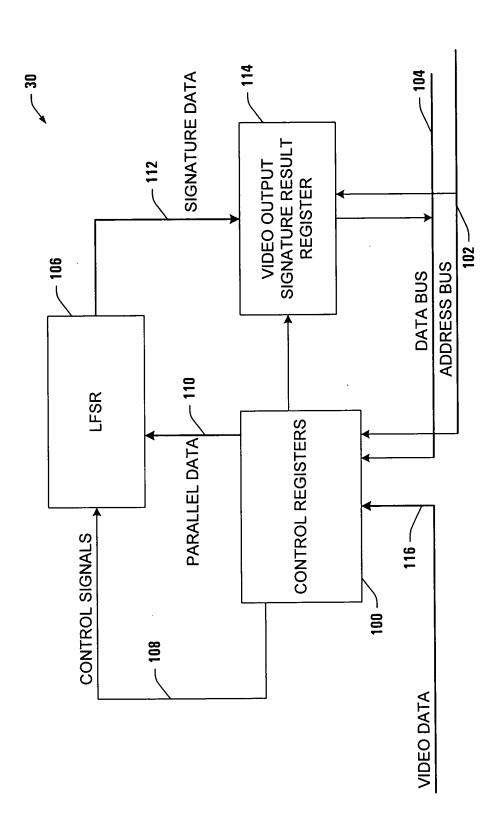
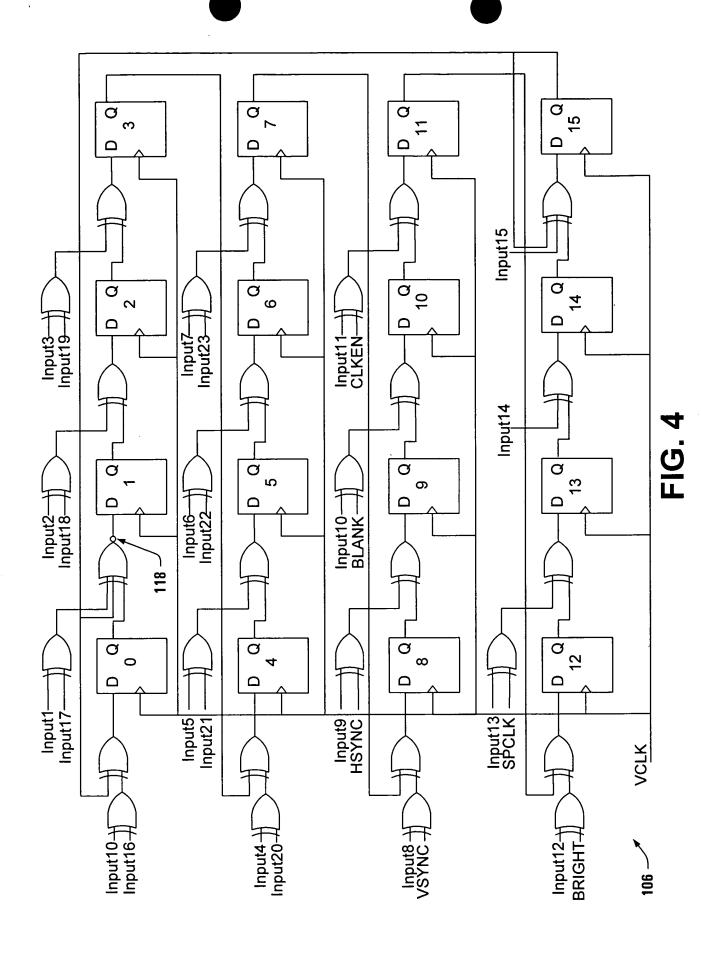


FIG. 2A





五G. 3



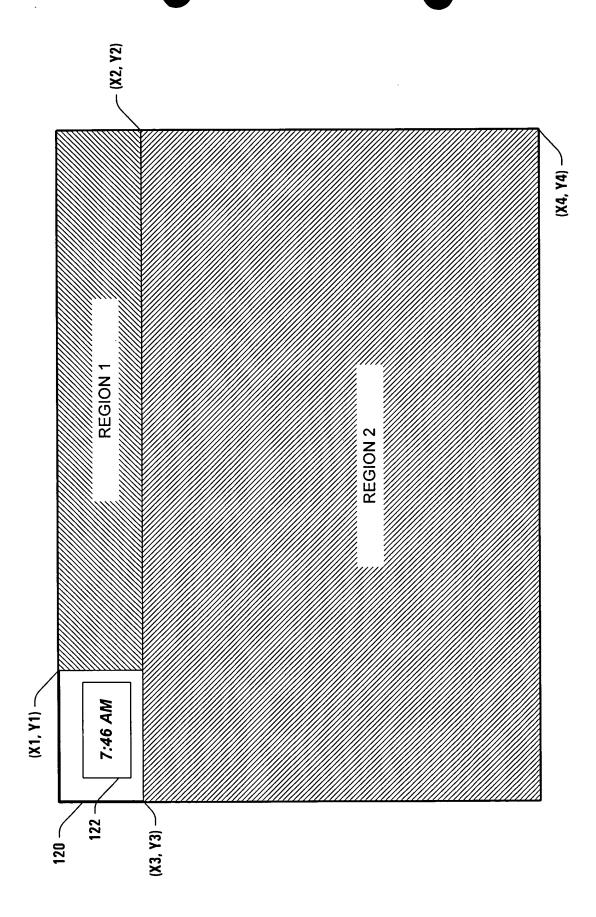


FIG. 5

16	RSVD
17	RSVD
18	RSVD RSVD
19	RSVD
20	RSVD
21	RSVD
22	RSVD
23	RSVD
24	RSVD
25	RSVD
26	RSVD
27	RSVD
28	RSVD
29	RSVD RSVD RSV
30	RSVD
31	RSVD

0	SIG
-	SIG
2	SIG
က	SIG
4	SIG
5	SIG
9	SIG
7	SIG
8	SIG
6	SIG
10	SIG
17	SIG
12	SIG
13	SIG
14	SIG
15	SIG

SIGVAL

130

FIG. 6A

		_	
16	PEN	0	PEN
17	PEN	—	PEN
18	PEN	2	PEN
20 19 18 17	PEN	က	PEN
20	PEN	4	PEN
21	PEN	5	PEN
22	PEN	9	PEN
23	PEN	7	PEN
25 24 23 22 21	VSYNC	8	PEN
25	HSYNC	6	PEN
26	CLKEN BLANK HSYNC VSYNC	10	PEN
27	CLKEN	7	PEN
28	BRIGH T	12	PEN
29	RSVD SPCLK BRIGH	13	PEN
31 30 29 28	RSVD	15 14 13 12	PEN
31	Ш Х	15	PEN

SIGCTL

FIG. 6B

132 —

16	STOP 0	0	START 0
17	STOP 1	-	START 1
18	STOP 2	2	START 2
19	STOP 3	က	START 3
20	STOP 4	4	START 4
21	STOP 5	5	START 5
22	STOP 6	9	START 6
23	STOP 7	7	START 7
24	STOP 8	8	START 8
25	STOP 9	6	START 9
26	STOP 10	10	START 10
27	RSVD	=======================================	RSVD
28	RSVD	12	RSVD
29	RSVD	13 12	RSVD
31 30 29 28	RSVD RSVD RSVD	15 14	RSVD RSVD RSVD
31	RSVD	15	RSVD

VSIGSTRTSTOP

134

FIG. 6C

		,	
16	STOP 0	0	START
17	STOP 1	-	STAR1
18	STOP 2	2	START 2
19	STOP 3	က	STAF
20	STOP 4	4	STAF
21	STOP 5	5	RT START 5
22	STOP 6	9	START 6
23	STOP 7	7	START 7
24	STOP 8	8	RT START
25	STOP 9	6	STAF 9
56	STOP 10	10	START 10
27	RSVD	11	RSVD
28	RSVD	12	RSVD
29	RSVD	13 12	RSVD
31 30	RSVD RSVD RSVD	14	RSVD RSVD RSVD
31	RSVD	15	RSVD

HSIGSTRTSTOP

136

FIG. 6D

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 RSVD RSVD RSVD RSVD RSVD RSVD RSVD R			_	
27 26 25 24 23 22 21 20 19 18 RSVD VCLR VCLR	16	VCLR 0	0	
27 26 25 24 23 22 21 20 19 18 RSVD VCLR VCLR	17	VCLR 1	-	HCLR 1
27 26 25 24 23 22 21 20 RSVD VCLR VCLR VCLR VCLR VCLR VCLR VCLR VCLR	18	VCLR 2	2	HCLR 2
27 26 25 24 23 22 21 RSVD VCLR VCLR VCLR VCLR VCLR VCLR 11 10 9 8 7 6 5 RSVD HCLR HCLR HCLR HCLR HCLR F	19	VCLR 3	က	HCLR 3
27 26 25 24 23 22 RSVD VCLR VCLR VCLR VCLR VCLR VCLR VCLR VCLR	20	VCLR 4	4	HCLR 4
27 26 25 24 23 RSVD VCLR VCLR VCLR VCLR 11 10 9 8 7 RSVD HCLR HCLR HCLR HCLR	21	VCLR 5	5	HCLR 5
27 26 25 24 RSVD VCLR VCLR VCLR 11 10 9 8 RSVD HCLR HCLR HCLR	22	VCLR 6	9	HCLR 6
27 26 25 RSVD VCLR VCLR 10 9 HCLR HCLR RSVD HCLR	23	VCLR 7	7	HCLR 7
27 26 RSVD VCLR 11 10 RSVD HCLR	24	VCLR 8	8	HCLR 8
27 RSVD 11	25	VCLR 9	6	HCLR 9
	26	VCLR 10	10	HCLR 10
31 30 29 28 RSVD RSVD RSVD 15 14 13 12 RSVD RSVD RSVD	27	RSVD	11	RSVD
31 30 29 RSVD RSVD 15 14 13 RSVD RSVD RSVD	28	RSVD	12	RSVD
31 30 RSVD RSVD 15 14 RSVD RSVD	29	RSVD	13	RSVD
31 RSVD 15	30	RSVD	14	RSVD
	31	RSVD	15	RSVD

SIGCLR

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FIG. 6E

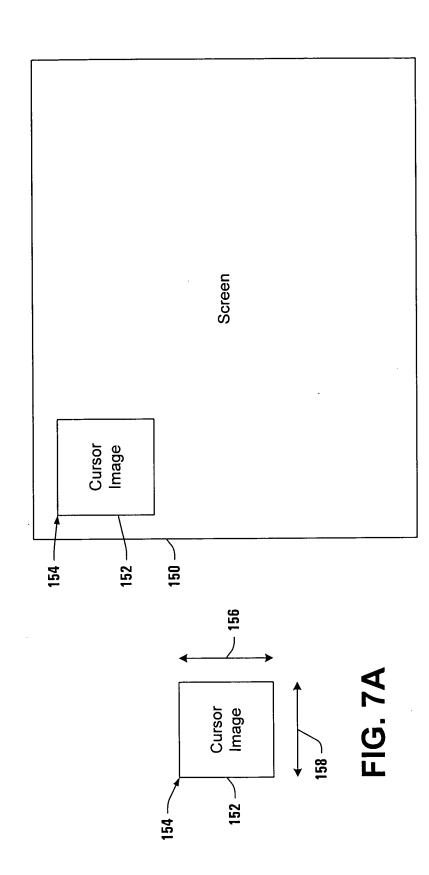


FIG. 7B

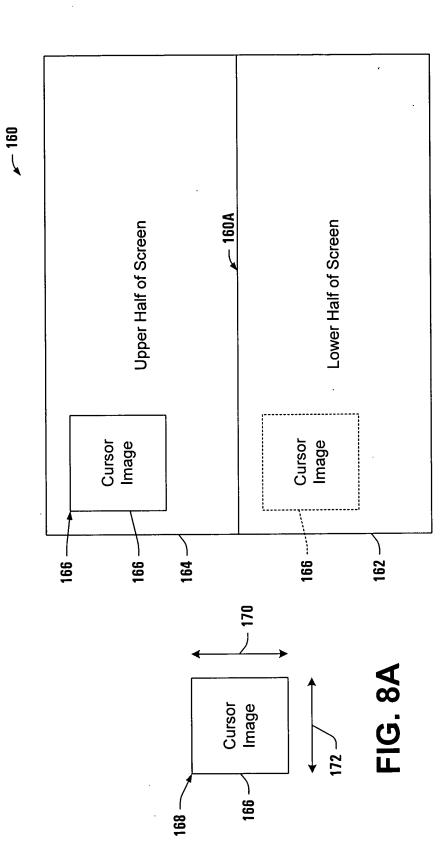


FIG. 8B

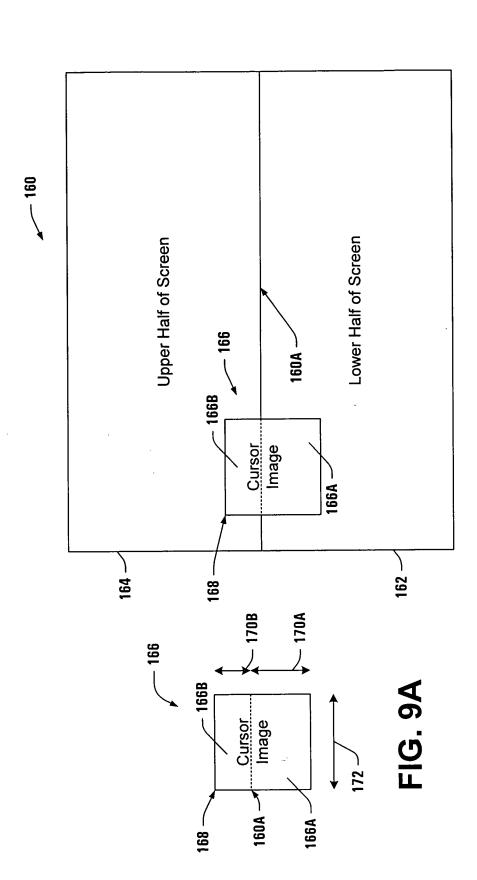
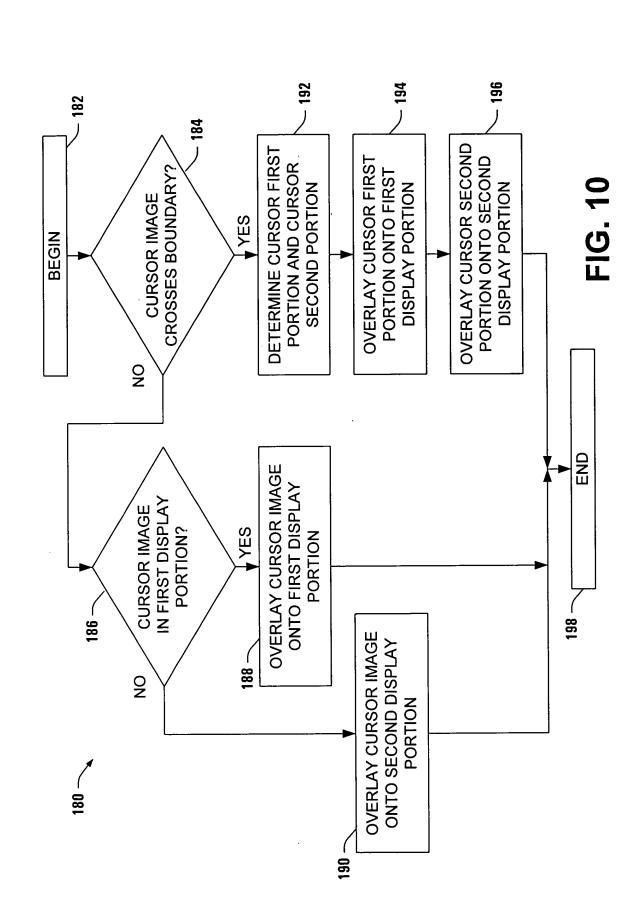


FIG. 9B



_			
16	ADR	0	¥
17	ADR	-	¥.
18	ADR	2	ADR
19	ADR	က	ADR
20	ADR	4	ADR
22 21 20 19 18 17	ADR	5	ADR
22	ADR	9	ADR
23	ADR	7	ADR
24	ADR	8	ADR
25 24	ADR	6	ADR
26	ADR	10 9	ADR
27	ADR	11	ADR
28	ADR	12	ADR
29 28	ADR	13	ADR
31 30	ADR	15 14 13	ADR
31	ADR	15	ADR

CURSOR_ADR_START

200

FIG. 11A

_			
16	ADR	0	NA
17	ADR	~	N A
18	ADR	2	ADR
19	ADR	3	ADR
20	ADR	4	ADR
21	ADR	5	ADR
22	ADR	9	ADR
23	ADR	7	ADR
24	ADR	80	ADR
25	ADR	6	ADR
26	ADR	5	ADR
27	ADR	=	ADR
28	ADR	12	ADR
29	ADR	13	ADR
31 30	ADR	4	ADR
31	ADR	15	ADR

CURSOR_ADR_RESET

202

FIG. 11B

16	RSVD	0	CWIDO
17	RSVD	~	CWID1
18	RSVD	2	CLINSO
19	RSVD	က	CLINS1
20	RSVD	4	CLINS5 CLINS4 CLINS3 CLINS2 CLINS1 CLINS0 CWID1 CWID0
21	RSVD	5	CLINS3
22	RSVD	9	CLINS4
27 26 25 24 23 22 21 20 19 18 17	RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	7	CLINS5
24	RSVD	8	CSTEP 0
25	RSVD	6	CSTEP CSTEP
26	RSVD	10 9	DLNS1 DLNS0
27	RSVD	11	DLNS1
28	RSVD	12	DLNS2
29	RSVD	13	DLNS3
30	RSVD	15 14	DLNS5 DLNS4 DLNS3 DLNS2
31	RSVD	15	DLNS5

CURSORSIZE

FIG. 11C

204 —

		_	
16	COLO	0	COLO
17	COLO	-	COLO
18	COLO R	2	COLO
19	COLO R	က	COLO
20	COLO	4	COLO
21	COLO	5	COLO
22	COLO	9	COLO
23	COLO	7	COLO
24	RSVD	80	COLO
25	RSVD	6	COLO
26	RSVD	10	COLO
27	RSVD	11	COLO
28	RSVD	12	COLO
29	RSVD	13	coLô R
30	RSVD	4	colo colô c
31	RSVD	15	COLO

FIG. 11D

CURSORCOLOR1 CURSORCOLOR2 CURSORBLINK1 CURSORBLINK2

206

_		-	
16	VLOC 10	0	NLOC 0
17	YLOC 1	~	xLoc 1
18	YLOC 2	2	XLOC 2
19	7LOC	က	XLOC 3
20	YLOC 4	4	XLOC 4
21	YLOC	22	XLOC 5
22	YLOC 6	9	XLOC 6
23	YLOC 7	7	XLOC 7
24	YLOC 8	8	XLOC 8
25	YLOC 9	6	6 XLOC
26	YLOC 10	10	XLOC 10
27	RSVD	11	RSVD
28	RSVD	12	RSVD
29	RSVD	14 13	RSVD
31 30	RSVD RSVD RSVD	4	CEN RSVD RSVD
31	RSVD	15	CEN

CURSORXYLOC

208

FIG. 11E

31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16				
30 29 28 27 26 25 24 23 22 21 20 19 18 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD		RSVD	0	VLOC 0
30 29 28 27 26 25 24 23 22 21 20 19 18 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	17	RSVD	-	YLOC 1
30 29 28 27 26 25 24 23 22 21 20 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	18	RSVD	2	
30 29 28 27 26 25 24 23 22 21 20 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	19	RSVD	က	YLOC 3
30 29 28 27 26 25 24 23 22 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD 14 13 12 11 10 9 8 7 6 RSVD RSVD RSVD RSVD RSVD RSVD RSVD 15 11 10 9 8 7 6	20	RSVD	4	YLOC 4
30 29 28 27 26 25 24 23 RSVD RSVD RSVD RSVD RSVD RSVD RSVD 14 13 12 11 10 9 8 7 RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	21	RSVD	5	
30 29 28 27 26 25 24 RSVD RSVD RSVD RSVD RSVD RSVD 14 13 12 11 10 9 8 RSVD RSVD RSVD YLOC YLOC RSVD RSVD RSVD 10 9 8	22	RSVD	9	YLOC
30 29 28 27 26 25 24 RSVD RSVD RSVD RSVD RSVD RSVD 14 13 12 11 10 9 8 RSVD RSVD RSVD YLOC YLOC RSVD RSVD RSVD 10 9 8	23	RSVD	7	YLOC 7
30 29 28 27 26 RSVD RSVD RSVD RSVD 14 13 12 11 10 RSVD RSVD RSVD 10 10	24	RSVD	8	YLOC 8
30 29 28 27 RSVD RSVD RSVD RSVD 14 13 12 11 RSVD RSVD RSVD RSVD RSVD RSVD	25	RSVD	6	
30 29 28 RSVD RSVD RSVD 14 13 12 RSVD RSVD RSVD	26		10	YLOC 10
30 29 RSVD RSVD 14 13 RSVD RSVD	27	RSVD	=	RSVD
30 RSVD F		RSVD	12	RSVD
31 30 RSVD RSVD 15 14 CLHEN RSVD		RSVD		RSVD
31 RSVD 15 CLHEN	30	RSVD	4	RSVD
	31	RSVD	15	CLHEN

CURSOR_DHSCAN_LH_YLOC

210

FIG. 11F

16	RSVD	0	RATE
17	RSVD	←	RATE
18	RSVD	2	RATE
20 19 18 17 16	RSVD	3	RATE
20	RSVD	4	RATE
21	RSVD	5	RATE RATE
23 22 21	RSVD	9	RATE
23	RSVD	7	RATE
25 24	RSVD	8	EN
25	RSVD	6	RSVD
26	RSVD	10 9	RSVD
27	RSVD	Ξ	VD RSVD RSVD RSVD
28	RSVD	12	RSVD
29	RSVD	13	RSVD
31 30 29	RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	4	RSVD RSVD RSVD RSV
31	RSVD	15	RSVD

CURSORBLINK

212

-16. 116

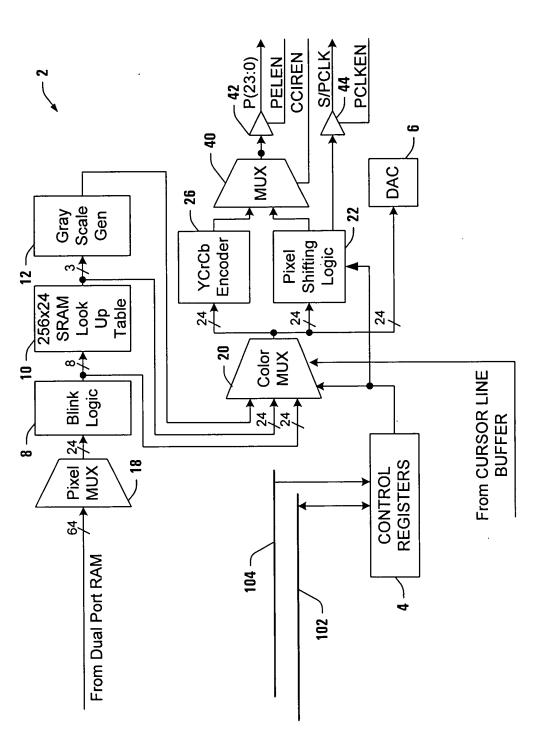


FIG. 12

16	RSVD	0	9 0
17	RSVD	←	2
18	RSVD	2	P2
20 19 18 17 16	RSVD	3	SO
20	RSVD	4	S1
21	RSVD	5	S2
22	RSVD	9	MO
23	RSVD	7	M1
24	RSVD	8	M2
25 24	RSVD	6	M3
26	RSVD	11 10 9	00
27	RSVD	11	2
28	RSVD	12	C2
29	RSVD	13	ငဒ
31 30 29	RSVD RSVD RSVD	15 14 13	RSVD DSCA
31	RSVD	15	RSVD

PIXELMODE

FIG. 13A

230 —

_	_		
16	RSVD	0	DAT
17	RSVD	~	DAT
18	RSVD	2	DAT
21 20 19 18 17	RSVD	3	DAT
20	RSVD	4	DAT
	RSVD	5	DAT
22	RSVD	9	DAT
23	RSVD	7	DAT
24	RSVD	8	RD
25	RSVD	6	RSVD
26	RSVD	10	RSVD
27	RSVD	7	RSVD
28	RSVD	12	RSVD
29	RSVD	13	RSVD
30	RSVD	4	RSVD
31	RSVD	15	RSVD

PARLLIFOUT

232

FIG. 13B

CNTO	0	DAT
CNT1	~	DAT
CNT2	2	DAT
CNT3	က	DAT
	4	DAT
ESTR T1	5	DAT
ESTR T2	9	DAT
ESTR T3	7	DAT
RSVD	8	RSVD
RSVD	6	RSVD
RSVD	10	RSVD
RSVD	7	RSVD
RSVD	12	RSVD
RSVD	13	RSVD
RSVD	4	RSVD RSVD RSV
RSVD	15	RSVD
	RSVD RSVD RSVD RSVD T3 T2 T1 T0 CNT3 CNT2 CNT1	D RSVD RSVD RSVD RSVD ESTR T3 ESTR T1 ESTR T0 CNT3 CNT2 CNT1 11 10 9 8 7 6 5 4 3 2 1

PARLLIFIN

234

FIG. 13C

	Т			T_	$\overline{}$		Т		Т		Т		Т	<u> </u>	<u>د</u>		Т	ช	_	٦	<u>۔</u>	<u>د</u>		Т	Þ	٤	딝
B (0)	4	<u>F</u>	B(0)	B(2)	_	P(2)	_	B(2)	_	B(4)	4	B(4)	4	P0(5)	B0(5)		4	Прред	P(S)	<u>B</u>	P0(7)	B0(7)		4	r Upper	P0(7)	B0(7)
<u> </u>		<u>E</u>	B(:)	B(3)		B(3)	\perp	B(3)	\perp	B(0)	\perp	B(0)	_	P0(6)	B0(6)	-		Upper	P(6)	9(e)	P0(15)	60(7)		\downarrow	r Upper) P0(15)	• G0(7)
P(2)		P(2)	B(2)	B(4)		B(4)		B(4)	\perp	B(:)	\downarrow	B(1)		P0(7)	B0(7)			Upper	P(7)	B(7)	P0(22)	R0(6) •		\perp	Upper	P0(22)	R0(6)
P(3)		P(3)	B(3)	B (0)		B(0)		B(5)		B(2)		B(2)		P0(13)	(S)			Upper	P(13)	ઉ	P0(23)	R0(7)			Upper	P0(23)	R0(7)
P(4)		P(4) B(4)		B(1)		B(1)		B(6)		B(3)		B(3)		P0(14)	(9) (20)			Upper	P(14)	કુ	P1(7)	B1(7)			Lower	P0(7)	B0(7)
P(5)	****	P(5) B(5) P(4) B(4)		B(2)		B(2)		B(7)		B(4)	1	B(4)		P0(15)	G0(7)			Upper	P(15)	<u>6</u>	P1(15)	G1(7)			Lower	P0(15)	G0(7)
P(6)		P(6)	B(6)	B(3)		B(3)		G(2)		G(0)		G(4)		P0(21)	R0(5)			Upper	P(21)	R(5)	P1(22)	R1(6)•			Lower	P0(22)	R0(6)
P(7)		P(7)	B(7)	B(4)		B(4)		Q(3)		Ĵ		6(0)		P0(22)	R0(6)			Upper	P(22)	R(6)	P1(23)	R1(7)			Lower	P0(23)	R0(7)
P(8)		P(8)	G(0)	G(4)		(2)		<u>\$</u>		G(2)		ਤੌ		P0(23)	R0(7)			Upper	P(23)	R(7)	P2(7)	B2(7)	_		Upper	P1(7)	B1(7)
P(9)	:	P(9)	G(1)	G(S)		G(3)		G(S)		G(3)		G(2)		P1(5)	B1(5)			Lower	P(5)	B(5)	P2(15)	G2(7)		\perp	Upper	P1(15)	G1(7)
P(10)		P(10)	Q(2)	G(0)		G(4)		(9)5		G(4)		£		P1(6)	B1(6)			Lower	P(6)	B(6)	P2(22)	R2(6) •			Upper	P1(22)	R1(6)
P(11)		P(11)	93	ફે		9		G(7)		ઉડ્ડ		6(4)		P1(7)	B1(7)			Lower	P(7)	B(7)	P2(23)	R2(7)	_		Upper	P1(23)	R1(7)
P(12)		P(12)	G(4)	6(2)		G(3)		R(2)		R(4)		R(4)		P1(13)	G1(S)			Lower	P(13)	ઉ	P3(7)	B3(7)			Lower	P1(7)	B1(7)
P(13)	***	P(13)	G(S)	G(3)		G(2)		R(3)		R(0)		R(0)		P1(14)	(9)19			Lower	P(14)	(9) (3)	P3(15)	(2)(1)			Lower	P1(15)	G1(7)
P(14)		P(14)	(9) (3)	G(4)		G(3)		R(4)		R(1)		R(1)		P1(15)	(2)			Lower	P(15)	G(7)	P3(22)	R3(6) •			Lower	P1(22)	R1(6) •
P(15)		P(15)	£(3)	ઈ		G(4)		R(5)		R(2)		R(2)		P1(21)	G1(5)			Lower	P(21)	R(5)	P3(23)	R3(7)			Lower	P1(23)	R1(7)
P(16)		P(16)	R(0)	R(2)		R(2)		R(6)		R(3)		R(3)		P1(22)	(9)(9)			Lower	P(22)	R(6)	P0(6)	B0(6) *			Upper	P0(6)	B0(6) *
P(17)		P(17)	R(1)	R(3)		R(3)		R(7)		R(4)		R(4)		P1(23)	R1(7)			Lower	P(23)	R(7)	P0(14)	. (9)05			Upper	P0(14)	G0(6) *
P(18)		P(18)	R(2)	R(4)		R(4)		×		x		×		P0(4)	B0(4) *			Upper	P(4) B(4)	٠	P1(6)	В1(6)•			Lower	P0(6)	B0(6) *
P(19)	:	P(19)	R(3)	R(0)		R(0)		×		x		×		P0(12)	R0(4) • G0(4) •			Upper	P(12)	G(4) •	P1(14)	B2(6) • G1(6) •			Lower	P0(14)	B1(6) * G0(6) *
P(20)	:	P(20)	R(4)	R(1)		R(1)		х		×		×		P0(20)				Upper	P(20)	R(4) *	P2(6)	B2(6) •			Upper	P1(6)	
P(21)	***	P(21)	R(5)	R(2)		R(2)		×		×		×		P1(4)	B1(4) *			Lower	P(4) B(4)	•	P2(14)	B2(6) *			Upper	P1(14)	G1(6) •
P(22)	:	P(22)	R(6)	R(3)		R(3)		×		×		×		P1(12)	R1(4) • G1(4) •			Lower	P(12)	G(4) •	P3(6)	B3(6) •			Lower	P1(6)	G1(6) • B1(6) •
P(23)	***	P(23)	R(7)	£(4)		R(4)		×		×	-	×		P1(20)	R1(4) •			Lower	P(20)	R(4) *	P3(14)	63(6)			Lower	P1(14)	G1(6) •
output mode		single pixel per	clock up to 24 bits	single 16-bit 565	pixel per clock	single 16-bit 555	pixel per clock	0x0 0x4 single 24 bit pixel	on 18 lines	single 16-bit 565	pixel on 18 lines	single 16-bit 555	pixel on 18 lines	progressive scan P1(20) P1(12)	2 pixels per shift	clock	dual scan				progressive scan	4 pixels per shift G3(6) * B3(6) *	clock	dual scan			
outpu			clock up	single 16	pixel p	single 10	pixel p	single 24	on 18	single 1	pixel on	single 1	pixel on	progres	2 pixels	บี	dua				progres	4 pixels	ช	dua			į
color	mode	0x0 0x4	0x8	0x5		910		0x0 0x4	0x8	0x5		9x0		0×0	8×0						0×0	0x8					
shift	mode	0x0		0x0		0x0		0x1		0x1		0x1		0,72							0x3						

FIG. 14A

			Т		9	,			T				r		· ·		人	
P0(7) B	•		\downarrow	Upper	P0(7) B	·	8		22	8	U B0		U R2	U GS	<u>©</u>	:		
P0(15)	8			Upper	P0(15)	ខ	පි		B3	8	8 n		U B3	URS	â	:	ᆜ	
P1(7) B1	•			Lower Lower	PO(15) PO(7) BO PO(15) PO(7) BC	·	22		3	B6	URO		n G3	U B6	D(2)	:	:	
P1(15)	• 15			Lower		8	B		8	ષ્ઠ	UBI		ÜR3	% D	D(3)	:	:	
P2(7) B2	•			Upper	P1(7) B1	•	5		B4	R6	UGI		U B4	U R6	D(4)	:	:	
PO(23) P7(15) P7(7) B7 P6(15) P6(7) B6 P5(15) P5(7) B5 P4(15) P4(7) B4 P3(15) P3(7) B3 P2(15) P2(7) B2 P1(15) P1(15) P1(7) B1 P0(15) P0(7) B0	G2 •			Upper	P1(15)	• [5]	R		દુ	B7	URI	_	U G	UB7	D(S)	:	:	
P3(7) B3				Lower	P1(7) B1	·	B2		R4	G)	U B2		U R4	U G7	D(6)	:	:	
P3(15)	• 3			Lower	P1(1S)	GI •	8		BS	R7	U G2		UBS	UR7	D(1)	:	:	
P4(7) B4	•			Upper	P3(15) P3(7) B3 P2(15) P2(7) B2 P2(15) P2(7) B2 P1(15) P1(7) B1	٠	×		×	×	L B0		L R2	r Gs	:	:	:	
P4(15)	<u>\$</u>			Upper	P2(15)	G 2	×		×	×	8		LB3	LRS	:	:	:	
P5(7) BS				Lower	P2(7) B2	•	×		×	×	L RO		L G3	L B6	:	:	:	
P5(15)	. S			Lower	P2(15)	G2 *	×		×	×	LBI		L R3	L G6	:	:	:	
P6(7) B6	•			Upper	P3(7) B3	٠	×		×	×	197		L B4	L R6	:	:	:	
P6(15)	&			Upper	P3(15)	G3 •	×		×	×	LRI		L G4	LB7	:	:	:	
P7(7) B7	•			Lower Lower	P3(15) P3(7) B3	٠	×		×	×	L B2		L.R4	LG7	:	:	:	
P7(15)	G7 *			Lower	P3(15)	G3 *	×		×	×	79 T		L BS	LR7	:	YSCL	:	
P0(23)	R0 *			Upper	P0(23)	R0 •	×		×	×	×		×	×	:	XECL	:	
P1(23)	<u>8</u>			Lower	P0(23)	R0 •	×		×	×	×		×	×	:	:	AC	
P2(23)	R2 *			Upper	P1(23)	R1 *	×		×	×	×		×	×	፡	:		
P3(23)	ъ.			Upper Lower	P2(23) P1(23)	R1 •	×		×	×	×		×	×	:	:	:	
P4(23)	R4 *					R2 *	×		×	×	×		×	×	:	:	:	
P5(23)				Lower	P3(23) P2(23)	R2 •	x		×	x	×		×	×	:	:	:	
P6(23)	R6 *			Upper	P3(23)	R3 *	x	,	×	×	×		×	×	:	:	:	
P7(23)	R7 *			Lower	P3(23)	υ. Σ	×		×	×	×		×	×	:	**	:	
progressive scan P7(23) P6(23) P5(23) P4(23) P3(23)	8 pixels per shift	clock	dual scan				2 2/3 pixels per	clock			Dual 2 2/3 pixels	per clock			CCIREN subs	LCDEN subs	ACEN subs	
0×0	0x8						0%	0x8			0%	8x0			:	:	:	
0x4			_				0x5				9x0				:	:	:	

FIG. 14B

These bits are an ORed combination of the bit value shown and the next significant bit below (This rounds the color value to nearest color).
 These bits do not get a substitute and are defined to the values controlled by the pixel output mode in the upper part of the table.
 These bits are pinned out in certain variants only.
 These bits are pinned out in certain variants only.

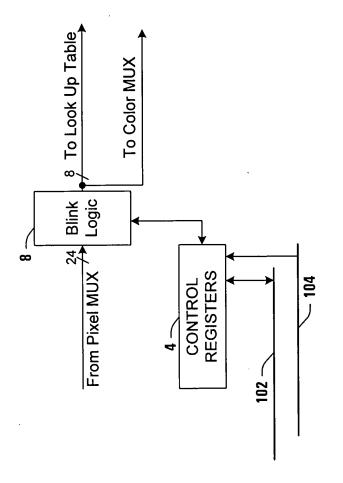


FIG. 15

16	RSVD	0	RATE
17	RSVD	~	RATE
18	RSVD	2	RATE
20 19 18 17	RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	အ	RSVD RSVD RATE RATE RATE RATE RATE RATE RATE
20	RSVD	4	RATE
21	RSVD	5	RATE
25 24 23 22 21	RSVD	9	RATE
23	RSVD	7	RATE
24	RSVD	8	RSVD
25	RSVD	6	RSVD
56	RSVD	10 9	RSVD
27	RSVD	7	RSVD
28	RSVD	12	RSVD
59	RSVD	13	RSVD
31 30 29 28	RSVD RSVD RSVD	15 14 13 12	RSVD RSVD RSVD RSVI
31	RSVD	15	RSVD

BLINKRATE

250 —

FIG. 16A

16	MASK	0	MASK
17	MASK	-	MASK
18	MASK	2	MASK
19	MASK	3 2	MASK
27 26 25 24 23 22 21 20 19 18 17 16	RSVD RSVD RSVD MASK MASK MASK MASK MASK MASK	4	MASK MASK MASK MASK MASK MASK MASK MASK
21	MASK	5	MASK
22	MASK	9	MASK
23	MASK	7	MASK
24	RSVD	8	MASK
25	RSVD		MASK
26	RSVD	11 10 9	MASK
27	RSVD	=	MASK
28	RSVD	12	MASK
59	RSVD	13	MASK
31 30	RSVD RSVD RSVD	15 14 13 12	MASK MASK MASK MASK
31	RSVD	15	MASK

BLINKMASK

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FIG. 16B

		_	
16	PATRN	0	PATRN
17	PATRN	-	PATRN
18	PATRN	2	PATRN
19	PATRN	3 2	PATRN
26 25 24 23 22 21 20 19 18 17 16	RSVD RSVD RSVD PATRN PATRN PATRN PATRN PATRN PATRN PATRN PATRN	4	PATRN
21	PATRN	5	PATRN
22	PATRN	9	PATRN
23	PATRN	7	PATRN
24	RSVD	8	PATRN
25	RSVD	6	PATRN
	RSVD	11 10 9 8	PATRN
27	RSVD	7	PATRN
28	RSVD	12	PATRN
29	RSVD	13	PATRN
31 30	RSVD RSVD RSVD	15 14 13 12	PATRN
31	RSVD	15	PATRN

BLINKPATRN

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FIG. 16C

16	P MASK	0	P MASK
17	P	-	P MASK
18	P MASK	2	P MASK
19	P MASK	က	P MASK
20	P MASK	4	P MASK
21	P MASK	5	P MASK
22	P MASK	9	P MASK
23	P	7	P MASK
24	RSVD	8	P MASK
25	RSVD	6	P MASK
26	RSVD	10	P MASK
27	RSVD	17	P MASK
28	RSVD	12	P
29	RSVD	13	AASK
30	RSVD RSVD RSVD	15 14	P MASK
31	RSVD	15	P MASK

PATTERNMASK

256 /

FIG. 16D

BGOFF	0	BGOFF
BGOFF	-	BGOFF
BGOFF	2	BGOFF
BGOFF	က	BGOFF
BGOFF	4	BGOFF
BGOFF	5	BGOFF
BGOFF	9	BGOFF
BGOFF	7	BGOFF
RSVD	8	BGOFF
RSVD	6	BGOFF
RSVD	10	BGOFF
RSVD	7	BGOFF
RSVD	12	BGOFF
RSVD	13	BGOFF
RSVD	4	BGOFF
RSVD	15	BGOFF
	RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	

BG_OFFSET

FIG. 16E

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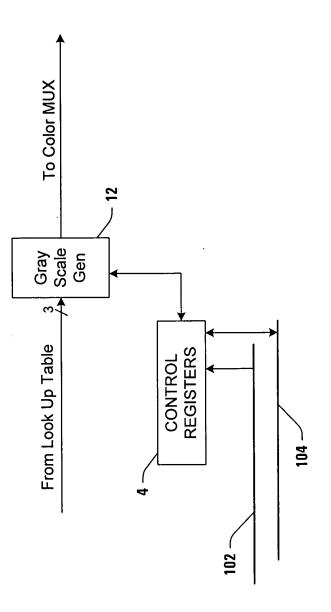


FIG. 17

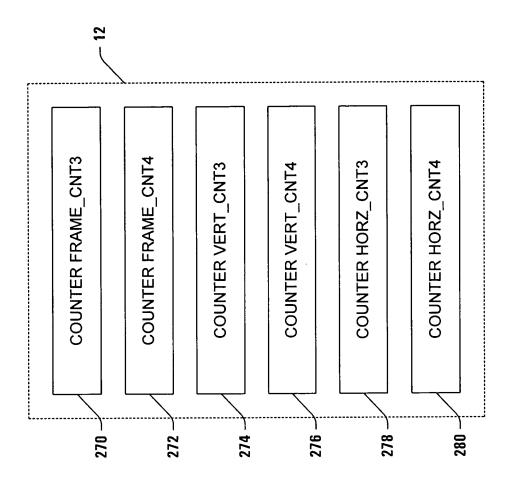


FIG. 18

HORZ	0	8
VERT	_	10
FRAME	2	D2
RSVD	3	D3
RSVD	4	D4
	5	D5
RSVD	မ	90
RSVD		D7
RSVD	ω	D8
	6	D3
RSVD	5	D10
RSVD	= =	D11
RSVD	12	D12
RSVD	13	D13
RSVD	4	D14
RSVD	15	D15
	RSVD RSVD RSVD RSVD RSVD RSVD RSVD RSVD	RSVD RSVD RSVD RSVD RSVD RSVD RSVD FRAME VERT 12 11 10 9 8 7 6 5 4 3 2 1

GRAYSCALE LUT

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FIG. 19

_	_	_	_	_		_	_	_	_		_		_					_																\neg
dress *4	Pixel	Value	000	001	010	011	100	101	110	111	000	00	010	011	100	101	110	111	000	001	010	011	100	101	110	111	000	100	010	011	100	101	110	111
GSLUT Address *4	FRAME		8	8	8	8	00	8	8	00	10	01	01	10	10	10	9	10	10	10	10	10	10	10	10	10	- 11	11	11	-11	11	11	- 11	11
8	8	П	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	00	80	8	8	8	8	8	8	8	8	8	8	8	8	8	8
8	9		10	10	10	5	5	ā	ă	5	5	ā	5	5	ă	5	٥	б	10	Ю	ы	ъ	ā	10	10	Ю	10	10	ō	10	5	ĭ	10	ő
8	9		8	20	20	25	8	g	8	20	25	8	20	20	20	8	g	20	20	8	20	8	8	20	20	20	20	20	03	20	20	20	20	20
8	=		8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	sa	63	8	8	8	80	ಣ	8	8	60	8
5	8		ž	ž	ž	ă	ă	ă	ă	z	ă	ă	ă	Z	ă	3	Z	ă	ž	ă	ă	ă	ă	2	10	ð	ð	ž	Z	8	ă	ð	ă	ă
2	10		8	8	8	8	8	8	8	8	8	8	g	8	8	8	g	8	8	g	8	8	8	8	8	90	8	8	8	8	8	8	8	8
2	10		8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	80	8	90	8	8	8	88	8	8	8	8
2	11		20	6	ľa	6	6	'n	6	ò	6	10	6	â	6	6	ā	6	20	20	6	۵	70	20	40	<i>1</i> 0	20	6	20	20	20	۵	20	0,
9	8		8	8	8	8	28	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	90	8	8	8	8	8	8	8	8
5	5		8	8	8	8	8	28	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	8	80	8	8	8	8
9	9		80	80	å	90	940	8	8	8	5	910	010	930	910	ē	910	930	9	010	010	010	010	010	910	010	D10	D10	010	010	D10	D10	010	010
9	Ξ		ā	ā	ĭ	ă	ã	ē	ā	Ē	iä	ē	110	10	ī	ä	5	12	10	110	110	110	D11	ĭ	110	110	110	ΙĮ	110	ıΩ	110	10	ē	110
=	8		510	012	D12	210	5	20	25	D12	015	210	210	210	012	012	012	012	012	D12	012	D12	D12	D12	Dt2	012	210	D12	D12	012	013	D12	012	D12
=	5		ë	DI3	613	ŝ	ş	<u>g</u>	g	ei3	013	550	5	g	ŝ	ei0	013	510	013	510	013	613	510	D13	D13	D13	510	D13	513	eig G	913	013	5	eg.
=	9		ă	š	ă	ă	ă	ă	ă	2	2	ž	ă	ş	ă	D14	914	10	D14	D14	ă	910	914	D14	P10	D14	D14	410	D14	D. 4	D14	\$10	410	4
E	Ξ		915	915	915	şä	ž	Dis	ä	915	915	g	g	ş	g	212	210	915	015	213	510	210	510	510	510	Pis Sig	935	915	510	DIS	915	510	910	\$10
VCNT (lines)	HCNT (pixels)	register address	base + 0x80	base + 0x84	base + 0x88	base + 0x8C	base + 0x90	base + 0x94	base + 0x98	base + 0x9C	base + 0xA0	base + 0xA4	base + 0xA8	base + 0xAC	base + 0xB0	base + 0xB4	base + 0xB8	base + 0xBC	base + 0xC0	base + 0xC4	base + 0xC8	base + 0xCC	base + 0xD0	base + 0xD4	base + 0xD8	base + 0xDC	base + 0xE0	base + 0xE4	base + 0xE8	base + 0xEC	base + 0xF0	base + 0xF4	base + 0xF8	base + 0xFC
Horz	ಶ		D16	D16	D16	D16	D16	016	D16	D16	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×
Vert	ζţ.		D17	017	D17	D17	D17	D17	D17	110	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×
FRAME	ភិ		018	D18	D18	D18	D18	D18	018	D18	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×	×
F.V.H def	for pixel	in value	8	9	010	91	8	101	110	=======================================									-															

FIG. 20

_			_	_	_	_	_	_	_	_
ddress *4	Pixel	Value	000	000	000	000	111	111	111	111
GSLUT Address *4	FRAME		00	01	10	11	00	01	10	11
8	8	8		•		۰	1	1	1	1
00	9	ы	•	•	•	۰	1	1	1	1
00	10	20	٥	٥	·		-	1	1	-
8	=	83	٥	·	۰		-	1	-	-
2	8	ă	0	۰	۰	•	-	-	-	-
٥	5	g	٥	۰	۰		-	-	-	-
5	5	8	۰	-	-	-	-	-	-	-
2	Ξ	'n	۰				-	-	-	-
5	8	8	۰	•	۰		-	-	-	-
5	9	8		۰		۰	-	-	-	-
5	9	9	۰		•	-	-	-	-	-
5	=	ă		۰	-	·	-	-	-	-
=	8	012	٥	°	۰	°	١	-	-	-
1	9	D13	٥	٥	•	•	1	-	-	-
=	10	D14	٥	٥	•	۰	-	-	_	_
=	Ξ	210	۰	٥	•	•	-	-	Ŀ	_
VCNT (lines)	HCNT (pixels)	register address	base + 0x80	base + 0xA0	base + 0xC0	base + 0xE0	base + 0x9C	base + 0xBC	base + 0xDC	base + 0xFC
Horz	ខី	D16	×				×		_	
Vert	ਹੱ	D17	×				×			
FRAME	ਨੋ	D18	×				×			

FIG. 21

FRAME 3

FRAME 2

FIG. 22

-	1	1	1
0	0	0	0
-	1	1	1
0	0	0	0
-			

FRAME 3

FRAME 2

 FIG. 2

FRAME 3

FRAME 2

-	_	_	_	_	_	_	_
	GSLUT Address "4	Pixel	Value	011	011	011	011
	GSLUTA	FRAME		00	01	10	11
	8	8	8	1	0	1	0
	8	10	ΙQ	1	0	0	١
	8	01	70	0	1	ı	0
	8	11	æ	0		0	١
	01	8	10	1	٥	1	٥
	5	10	8	0	ŀ	ŀ	۰
	5	10	8	-	۰	۰	-
	5	11	20	۰	٠	ŀ	-
	10	8	8	۰	-	-	•
	9	10	8	۰	-	•	-
	9	10	D10	-	•	-	۰
	5	=	110	1	0	٥	-
	Ξ	8	012		۰	•	-
	11 11 11	6	D13	۰	-	٥	-
	=	10	D14	-	٥	1	-
	=	=	015	•	-	-	٥
	VCNT (lines)	HCNT (pixels)	register address	base + 0x8C	base + 0xAC	base + 0xCC	base + 0xEC
	Horz	ζţ	D16	-			
	Vert	τ̈́	D17	-			
	FRAME	ಕ	D18	-			

FIG. 25

310

I

FRAME 0

 α \propto > ш

FRAME 1

FRAME 2

	I
214	1

FRAME 0

c	0	1	0
0	0	0	1
I	-	0	0
4	>	ш	22

FRAME 1

0	0	1
1	1	0
0	0	0

FRAME 2

1	0	0
0	0	0
0	1	1

	Address *4	Pixel	Value	010	010	010	010
	GSLUT Ado	FRAME		00	10	10	11
ŀ	8	8	8		0	0	×
۱	8	10	5	۰	-	٥	×
	8	10	22	•	0	•	×
ĺ	8	11	8	×	×	×	×
	5	8	Z	·	۰	1	×
ĺ	5	01	8	•	-	٥	×
ĺ	2	10	8	-	۰	•	×
	5	11	20	×	×	×	×
ĺ	2	8	8	-	-	-	×
	2	9	8	-		۰	×
	2	9	8		-	۰	×
	9	Ξ	5	×	×	×	×
	=	8	015	×	×	×	×
	=	2	513	×	×	×	×
	-	5	40	×	×	×	×
	11	Ξ	015	×	×	×	×
	VCNT (lines)	HCNT (pixels)	register address	base + 0x88	base + 0xA8	base + 0xC8	base + 0xE8
	Horz	ਹੋ	D16	0			
	Vert	ਠੋ	D17	0			
	FRAME	ප්	D18	0			

FIG. 28

316

1	1	0
1	0	0
0	0	0
0	-	-

FRAME 2

FIG. 29

-	_		$\overline{}$		_	_
GSLUT Address *4	Pixel	Value	010	010	010	010
GSLUTA	FRAME		00	10	10	11
8	8	8	1	0	0	×
8	9	10	0	1	0	×
8	10	70	۰	٥	Ŀ	×
8	11	60	۰	۰	-	×
10	8	ă	•	ŀ	-	×
10	5	8	٥	-	۰	×
0	5	8	-	°	°	×
10	7	20	-	۰	-	×
10	8	8		•	-	×
10	10	8	-			×
10 10 10 10	9	010	•	-	٥	×
10	1	110	۰	-	۰	×
11	8	D12	×	×	×	×
11	9	013	×	×	×	×
11 11 11 11	10	014	×	×	×	×
11	Ξ	g	×	×	×	×
VCNT (lines)	HCNT (pixels)	register address	base + 0x88	base + 0xAB	base + 0xC8	base + 0xE8
Horz	ಕ	910	0			
Vert	పే	710	0			
FRAME	పే	D18	0			

FIG. 30

320 —

9	E (3)															7	7		\neg										T	T
o message of	vertical Fran Rate (Hz)	400	230	09	80	20	50	09	75	75	75	20	85	80	85	72	8	9	70	9	70	65	9	09	50	9	09	55	\$ 5	24
Pixel Shift Clock	mequency (MHz)	0.25	0.5	0.5	6.4	4	80	4	3	24	24	ΥN	Ϋ́	40	۷N	30	ΑN	85	ΝΑ	06	110	135	۸N	135	20	ΑN	135	ΝΑ	16.875	16.875
-	pixels per shift clock	8	4	4	-	-	-	4	8		1	1	1	1	1	2	1	1	1	1	7	₩.	+	-	1	1	1	,	8	20 0
	Display Data format	monochrome	monochrome	monochrome	analog	4 bit RGB	4 bit RGB	monochrome	grayscale	18 bit RGB	18 bit RGB	analog	analog	18 bit RGB	analog	18 bit RGB	analog	18 or 24 bit RGB	analog	18 or 24 bit RGB	18 or 24 bit RGB	18 or 24 bit RGB	analog	18 or 24 bit RGB	24 bit RGB	analog	24 bit RGB	analog	monochrome	monochrome
	Frame Butter Storage format	4 bpp	4 bpp	4 bpp	g bpp	4 bit RGB	4 bit RGB	4 bpp	4 or 8 bpp	8 or 16 bpp	8, 16, or 24 bpp	4 bpp	4 bpp																	
Video Clock	frequency (MHz)	2	2	2	6.4	4	8	16	24	24	24	25.175	32	40	20	09	7.5	85	110	06	110	135	135	135	50	99	135	135	135	135
	Horizontal Vertical Resolution x Resolution	×	128 × 64	256 x 128	320 × 234	320 × 240	640 x 240	640 × 400	640 x 480	640 x 480	640 x 480	640 × 480	640 × 480	800 × 600	800 × 600	1024 x 768	1024 x 768	1280 × 1024	1280 x 1024	1400 x 1024	1400 x 1050	1600 x 1200	1600 x 1200	1900 x 1200	1280 x 720	1280 x 720	1920 x 1080	1920 x 1080	2048 x 1536	2560 x 2048
	Display Type	VFD	CD	CCD	"QVGA" TFT LCD	QVGA STN LCD	HVGA STN LCD	"VGA" DC Plasma	VGA EL	VGA STN LCD	VGATFT LCD	VGA CRT	VGA CRT	SVGA TFT LCD	SVGA CRT	XGA TFT LCD	XGA CRT	SXGA TFT LCD	SXGA CRT	SXGAW TFT LCD	SXGA+ TFT LCD	UXGA TFT LCD	UXGA CRT	UXGAW TFT	HDTV-2 LCD	HDTV-2 CRT	HDTV-4 LCD	HDTV-4 CRT	OXGA LCD	QSXGA LCD

FIG. 31

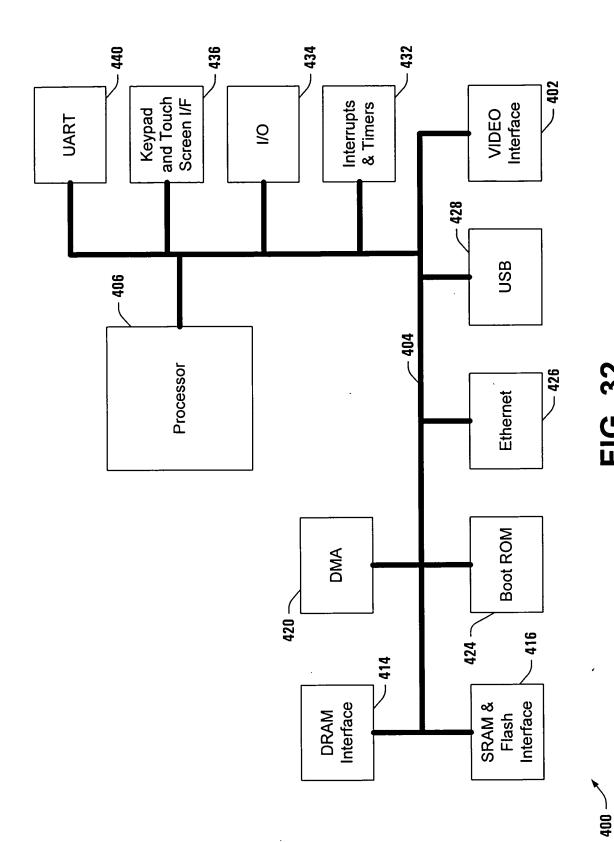


FIG. 32